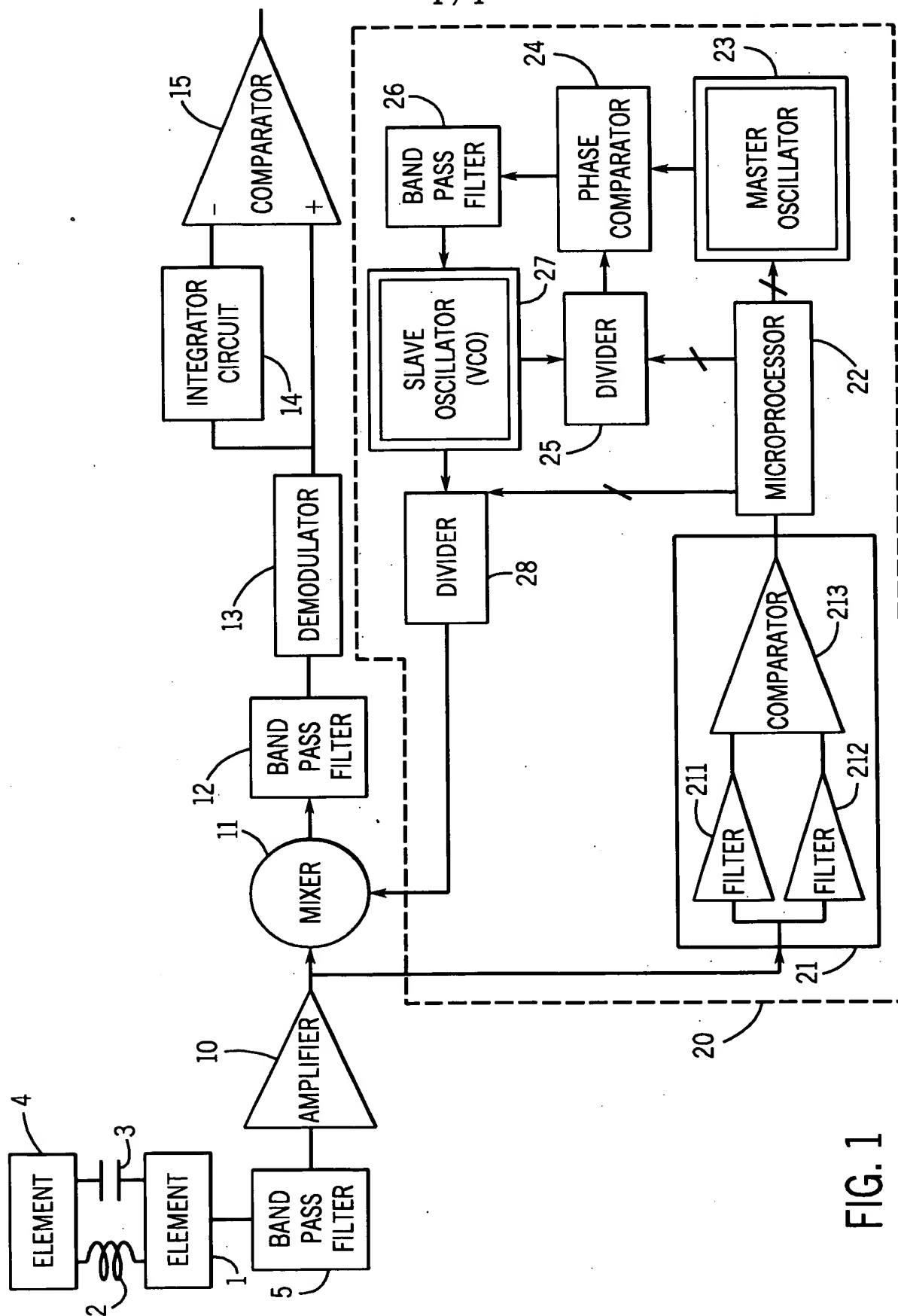
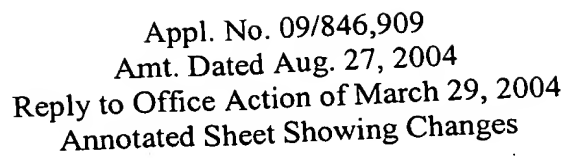


Amendments to the Drawings:

The drawing sheet attached in connection with the above-identified application containing Figure 1 is being presented as a new formal drawing sheet to be substituted for the previously submitted drawing sheet or sheets. The drawing figure 1 has been amended. Appended to this amendment is an annotated copy of the previous drawing sheet which has been marked to show changes presented in the replacement sheet of the drawing.

The specific changes which have been made to Figure 1 are the addition of descriptive labels to elements, in particular: block 1 has been labeled ELEMENT, block 4 has been labeled ELEMENT, block 5 has been labeled BAND PASS FILTER, block 10 has been labeled AMPLIFIER, block 11 has been labeled MIXER, block 12 has been labeled BAND PASS FILTER, block 13 has been labeled DEMODULATOR, block 14 has been labeled INTEGRATOR CIRCUIT, block 15 has been labeled COMPARATOR, block 211 has been labeled FILTER, block 212 has been labeled FILTER, block 213 has been labeled COMPARATOR, block 22 has been labeled MICROPROCESSOR, block 23 has been labeled MASTER OSCILLATOR, block 24 has been labeled PHASE COMPARATOR, block 25 has been labeled DIVIDER, block 26 has been labeled BAND PASS FILTER, block 27 has been labeled SLAVE OSCILLATOR (VCO) and block 28 has been labeled DIVIDER.





The diagram illustrates a PLL system with the following components and connections:

- Input Stage:** An **ELEMENT** (4) is connected to a **BAND PASS FILTER** (5). The output of the filter goes through an **AMPLIFIER** (10) and a **MIXER** (11).
- Feedback Path:** The output of the mixer (11) passes through a **BAND PASS FILTER** (12) and a **DEMODULATOR** (13). The demodulator's output is fed into an **INTEGRATOR CIRCUIT** (14), which then connects to a **COMPARATOR** (15). The comparator's output is fed back to the input of the amplifier (10).
- Reference and Frequency Synthesis:** A **MASTER OSCILLATOR** (23) provides a reference signal to a **DIVIDER** (25) and a **PHASE COMPARATOR** (24). The phase comparator's output is fed into a **SLAVE OSCILLATOR (VCO)** (26). The VCO's output passes through a **DIVIDER** (28) and is fed into the mixer (11).
- Control Logic:** A **MICROPROCESSOR** (22) is connected to the divider (25), the phase comparator (24), and the master oscillator (23). It also controls a **COMPARATOR** (213) within a block (20). This block (20) also contains two **FILTER** units (211 and 212).
- Handwritten Annotations:** The text "Descriptive label added" is written multiple times in various orientations around the diagram, pointing to several components.

FIG. 1